



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re application of: Sokolov et al.

Attorney Docket No.: SUN1P816/P5614

Application No.: 09/818,096

Examiner: SHAH, Niles R.

Filed: March 26, 2001

Group: 2127


Title: LOADING ATTRIBUTE FOR PARTIAL
LOADING OF CLASS FILES INTO VIRTUAL
MACHINES

Confirmation No.: 2837

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on March 11, 2005 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: _____


Susan W. Xu

**INFORMATION DISCLOSURE STATEMENT
AFTER FINAL ACTION OR NOTICE OF ALLOWANCE
(37 CFR §§ 1.56 AND 1.97(d))**

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is being filed after the mailing date of final action under §1.113 or a notice of allowance under §1.311, but before payment of the issue fee.

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180.00 OP

Accompanying this Information Disclosure Statement is the fee set forth in 37 CFR 1.17(p).

If fees are due, enclosed is our Check No. 25341 for \$180.00 in payment of the Information Disclosure Statement Fee. If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. SUN1P816).

Respectfully submitted,

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Form 1449 (Modified)	Atty Docket No. SUN1P816/P5614	Application No.: 09/818,096
Information Disclosure Statement By Applicant	Applicant: Sokolov et al.	
(Use Several Sheets if Necessary)	Filing Date March 26, 2001	Group 2151

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
	A	5,815,718	09/29/98	Theron D. Tock			05/30/96
	B	5,920,720	07/06/99	Toutonghi et al.			02/25/97
	C	6,003,038	12/14/99	Tao Shinn Chen			03/31/97
	D	6,081,665	06/27/00	Nilsen et al.			12/19/97
	E	6,096,095	08/01/00	Mark halstead			06/04/98
	F	6,151,618	11/21/00	Wahbe et al.			06/18/97
	G	6,163,780	12/19/00	Richard A. Ross			04/01/98
	H	6,202,208 B1	03/13/01	M. Holiday, Jr.			09/29/98
	I	6,205,578 B1	03/20/01	Daniel D. Grove			08/14/98
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	AA	6,738,977 B1	05/18/04	Berry et al.			05/31/00
Examiner				Date Considered			

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub- class	Translation	
							Yes	No
	BB	EP0810522A2	12/03/97	EPO				
	CC	EP0943989A2	09/22/99	EPO				
	DD	WO99/31576	06/24/99	PCT				

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	EE	Case B: "Implementing The Java Virtual Machine <i>Java's Complex Instruction Set Can Be Built in Software or Hardware</i> ," Microprocessor Report, Vol.10, No. 4, March 25, 1996, pages 12-17. XP-000987276
	FF	McNeley KJ et al., "Emulating a Complex Instruction Set Computer With a Reduced Instruction Set Computer," IEEE Micro, IEEE Inc. New York, US, vol. 7, no. 1, February 1987, Pages 60-71. XP-000827611
	GG	Jean-Paul Billon, "JEFFWEG4 (J Executable File Format), Release 1.0 Achievements, 9/29/2000," J Consortium Jeff Working Group, 'Online! 22 February 2001, page 1-24. XP-002208358
	HH	Jean-Paul Billon, "J Executable File Format (JEFF) Specification, Draft," J Consortium Jeff Working Group, 'Online! 22 February 2001, page 1-43. XP-002208357
	II	Chang et al., "EJVM: an economic Java run-time environment for embedded devices," Software Practice & Experience, 2001, John Wiley & Sons, Ltd., Vol. 31, pages 129-146. XP-000987539
	JJ	Gosling et al., "The Java™ Language Specification", August 1, 1996, pages 215-236. XP 002042923
	KK	Zhenyu Qian, "A Formal Specification of Java™ Virtual Machine Instructions," Technical Report, University Bremen; November 1997, pages 1-32. XP-002255760.
	LL	Markus Dahm, "Byte Code Engineering" Freie University Berlin, 1999. XP-002262007
	MM	Meyer Et Al., "Java Virtual Machine" O'Reilly & Associates, Inc., 1997. XP-002262008
	NN	Bill Venners, "How the Java virtual machine handles method invocation and return" Java World, June 1997, pages 1-10
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.